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PTO/SB/05 (1/98)

UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No. 6202-41

First Inventor or Application Identifier: Pui Lun LAU

Title: COMPUTER MULTIPLE COMMUNICATIONS PORT UNIT

Express Mail Label No.

APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent application contents.

- 1 ☒ Fee Transmittal Form (e.g., PTO/SB/17)
(Submit an original, and a duplicate for fee processing)
- 2 ☒ Specification Total Pages [21]
(preferred arrangement set forth below)
 - Descriptive title of the invention
 - Cross References to Related Applications
 - Statement Regarding Fed sponsored R & D
 - Reference to Microfiche Appendix
 - Background of the Invention
 - Brief Summary of the Invention
 - Brief Description of the Drawings (if filed)
 - Detailed Description
 - Claim(s)
 - Abstract of the Disclosure
- 3 ☒ Drawing(s) (35 USC 113) Total Sheets [3]
- 4 ☒ Oath or Declaration Total Pages [2]
 - a. ☒ Newly executed (original or copy)
 - b. ☐ Copy from a prior application (37 CFR 1.63(d))
(for continuation/divisional with Box 17 completed)
[Note Box 5 below]

☐ DELETION OF INVENTOR(S)
Signed statement attached deleting inventor(s) named in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b)
- 5 ☐ Incorporation By Reference (useable if Box 4b is checked)
The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4b, is considered to be part of the disclosure of the accompanying application and is hereby incorporated by reference therein

ADDRESS TO:

Commissioner for Patents
Box Patent Application
Washington, DC 20231

- 6 ☐ Microfiche Computer Program (Appendix)
- 7 Nucleotide and/or Amino Acid Sequence Submission
(if applicable, all necessary)
 - a ☐ Computer Readable Copy
 - b ☐ Paper Copy (identical to computer copy)
 - c ☐ Statement verifying identity of above copies

ACCOMPANYING APPLICATION PARTS

- 8 ☒ Assignment Papers (cover sheet & document(s))
- 9 ☐ 37 CFR 3.73(b) Statement ☐ Power of Attorney
(when there is an assignee)
- 10 ☐ English Translation Document (if applicable)
- 11 ☐ Information Disclosure Statement ☐ Copies of IDS
(IDS)/PTO-1449 Citations
- 12 ☐ Preliminary Amendment
- 13 ☒ Return Receipt Postcard (MPEP 503)
(Should be specifically itemized)
- 14 ☐ *Small Entity Statement(s) ☐ Statement filed in prior application,
Status still proper and desired
(PTO/SB/09-12)
- 15 ☐ Certified Copy of Priority Document(s)
(if foreign priority is claimed)
- 16 ☐ Other

*A new statement is required to be entitled to pay small entity fees, except where one has been filed in a prior application and is being relied upon

17. If a **CONTINUING APPLICATION**, check appropriate box, and supply the requisite information below and in a preliminary amendment

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No. ____

Prior application information: Examiner: ____ Group/Art Unit: ____

18. CORRESPONDENCE ADDRESS

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Date: November 9, 2000

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FEE TRANSMITTAL

Patent fees are subject to annual revision on October 1
These are the fees effective October 1, 1997 Small Entity
payments *must* be supported by a small entity statement,
otherwise large entity fees must be paid See Forms
PTO/SB/09-12

Complete If Known

Application Number	Not yet assigned
Filing Date	November 9, 2000
First Named Inventor	Pui Lun LAU
Examiner Name	Not yet assigned
Group Art Unit	Not yet assigned

TOTAL AMOUNT OF PAYMENT	\$1360 00	Attorney Docket Number	746202-000041
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METHOD OF PAYMENT (check one)

1. ☒ The Commissioner is hereby authorized to charge indicated fees and credit any over payments to.
Deposit Account No. 19-2380 (6202-0041)
Deposit Account Name NIXON PEABODY, LLP
- ☐ Charge Any Additional Fee Required Under 37 CFR 1.16 and 1.17
☐ Charge the Issue Fee Set in 37 CFR 1.18 at the Mailing of the Notice of Allowance
2. ☒ Payment Enclosed:
☒ Check ☐ Money Order ☐ Other

FEE CALCULATION (continued)**3 ADDITIONAL FEES**

Large Entity Small Entity

				Fee Code	Fee (\$)	Fee Code	Fee (\$)	Fee Description	Fee Paid
1. <input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge indicated fees and credit any over payments to. Deposit Account No. 19-2380 (6202-0041) Deposit Account Name NIXON PEABODY, LLP				105	130	205	65	Surcharge-late filing fee or oath	
				127	50	227	25	Surcharge-late provisional filing fee or cover sheet	
				139	130	139	130	Non-English specification	
				147	2,520	147	2,520	For filing a request for reexamination	
				112	920*	112	920*	Requesting publication of SIR prior to Examiner action	
				113	1,840*	113	1,840*	Requesting publication of SIR after Examiner action	
				115	110	215	55	Ext for reply within first month	
				116	390	216	195	Ext for reply within second mth	
				117	890	217	445	Ext for reply within third mth	
				118	1,390	218	695	Ext for reply within fourth mth	
2. <input checked="" type="checkbox"/> Payment Enclosed: <input checked="" type="checkbox"/> Check <input type="checkbox"/> Money Order <input type="checkbox"/> Other				128	1,890	228	945	Ext for reply within fifth month	
				119	310	219	155	Notice of Appeal	
				120	310	220	155	Filing brief in support of appeal	
				121	270	221	135	Request for Oral Hearing	
				138	1,510	138	1,510	Petition to institute public use proceeding	
				140	110	240	55	Petition to revive-unavoidable	
				141	1,240	241	620	Petition to revive-unintentional	
				142	1,240	242	620	Utility issue fee (or reissue)	
				143	440	243	220	Design issue fee	
				144	600	244	300	Plant issue fee	
1. BASIC FILING FEE Large Entity Small Entity Fee Fee Fee Description Fee Paid Code (\$) Code (\$) [710.00] 101 710 201 355 Utility filing fee 106 320 206 160 Design filing fee 107 490 207 245 Plant filing fee 108 710 208 355 Reissue filing fee 114 150 214 75 Provisional filing fee SUBTOTAL (1) \$710 00				122	130	122	130	Petitions to the Commissioner	
				123	50	123	50	Petitions related to provisional applications	
				126	240	126	240	Submission of IDS	
				581	40	581	40	Recording each patent assignment per property (times number of properties)	
				146	710	246	355	Filing a submission after final rejection (37 CFR 1.129(a))	
				149	710	249	355	For each additional invention to be examined (37 CFR 1.129(b))	
								Other _____	
								Other _____	
								*Reduced by Basic Filing Fee Paid	
								SUBTOTAL (3)	\$40 00
2. EXTRA CLAIM FEES Total Claims $\frac{45}{25} - 20^{**} = \frac{25}{2} \times \$18 = \$450.00$ Independent Claims $\frac{5}{2} - 3^{**} = \frac{2}{2} \times \$80 = \$160.00$ Multiple Dependent Claims $\frac{0}{0} = 0$ **for number previously paid, if greater, For Reissues, see below Large Entity Small Entity Fee Fee Fee Description Code (\$) Code (\$) Claims in excess of 20 103 18 203 9 Independent claims in excess of 3 102 80 202 40 Multiple dependent claim 104 270 204 135 **Reissue independent claims over original patent 109 80 209 40 **Reissue claims in excess of 20 and over original patent 110 18 210 9 SUBTOTAL (2) \$610 00									

SUBMITTED BY**Complete (if applicable)**

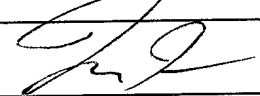
Typed or Printed Name

Marc S. Kaufman

Reg. Number

35,212

Signature



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11/09/2000

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COMPUTER MULTIPLE COMMUNICATIONS PORT UNIT

FIELD OF THE INVENTION

The invention relates to networked intelligent electronic devices and more particularly, to a multiple communications port unit for coupling plural intelligent electronic devices to computers over a network connection.

BACKGROUND OF THE INVENTION

Protective relays are commonly used to protect electrical power distribution systems. A simple protective relay can be an electro-mechanical contact relay having an energizing circuit coupled to the load on an electric power distribution line. When the load on a portion of the system exceeds certain parameters, i.e., a fault is present, the relay is energized to operate a circuit breaker or the like coupled to the power distribution line to thereby isolate a portion of the electrical distribution system having the fault. The contacts of the relay can perform various functions such as tripping a circuit breaker, generating an alarm or providing a signal to another protective relay. For the most part, such simple relays only provide the contact operation as an output. They do not provide any other indication of the conditions which produced activity in the output contacts.

For instance, the principal of operation of a distance measuring relay is that the distance of a fault can be determined by a comparison of the complex impedance of the line derived from the current and voltage at the relay to a reach characteristic. If this comparison indicates that there is a fault within the protection zone of the relay, a trip contact is actuated. Accordingly, "intelligent relays" have been developed having microprocessors capable of evaluating complex impedance or other variables to protect the electrical distribution system by isolating faults and to transmit diagnostic data and the like to computers or other devices for evaluation and display purposes. Typically, substations in an electric power distribution system have many protective relays. The intelligent relays can be programmed to perform various protection functions. For instance, digital distance relays and overcurrent relays are in use. These

relays are capable of providing a great deal of information. For instance, data indicating the location of the fault, and the current and voltage at the fault are available from intelligent distance relays. Similarly, current information is available from an intelligent overcurrent relay. This information is often transmitted to a remote computer for control and report generation. Conventionally, this is accomplished over an RS-232 channel or other serial communications channel for each relay. In addition to relays, other intelligent electronic devices, i.e. control devices having microprocessors capable of executing commands and/or collecting data, have been developed.

Accordingly, multiple serial port units are utilized to couple multitudes of intelligent electronic devices to remote a computer. Such units include multiple serial ports, 8 for example, and a single Ethernet port for coupling the serial ports to a network communications link. The Ethernet port is coupled to a computer using standard cables and protocols, e.g. a twisted pair. The serial ports are each connected to an intelligent electronic device. The multiple serial port unit uses known hardware and software to map the serial ports over the Ethernet connection to appear as local serial ports to the computer. Accordingly plural intelligent protective relays can be coupled to a computer over a great distance by an Ethernet connection.

However, intelligent electronic devices and associated computer systems are applied under hazardous conditions. Particularly, electrical distribution systems are inherently in environments that include temperature changes, high EMF, and other environmental hazards. Accordingly, computers, network connections, and other components often fail intermittently or permanently in electrical distribution systems.

If a computer or network connection over which an intelligent protective relay is communicating fails, the operator may not have control of devices such as circuit breakers, disconnect switches, and the like. Also, the operator might not know the status of their devices and may lose centralized control of an entire substation. Accordingly, the protective relay may not function properly and equipment damage or injury of personnel may result. Accordingly, there is a need for failure tolerance in a control system for protective relays in an electric power distribution system.

It is known to provide network failure tolerance using redundant computers i.e., servers, and to switch over to a backup computer when a primary computer fails. Also, it is known to send separate packets of data over different paths in an Ethernet connection. However, these systems and methods are not truly redundant because they rely on the computers for fault detection and a single Ethernet connection for communications.

SUMMARY OF THE INVENTION

Accordingly, the present invention relates to a multiple port unit adapted for coupling two or more networked computers to multiple peripheral devices through one or more network connections, wherein the multiple port unit monitors the status of the network connections and the coupled computers and maintains communication with at least one of the computers via a selected link.

More specifically, the present invention relates to a multiple port unit having at least one redundant power supply module, multiple peripheral communication ports for communicating with peripheral devices, at least two network ports for connection with one or more network hubs, through which remote control computers are networked, and a control unit configured to monitor communication links through the network ports and select a desired link for communication with the remote control computers to thereby coupling the peripheral devices to the remote control computers.

Another aspect of the present invention relates to a method for coupling a plurality of peripheral devices to a plurality of remote control computers via a plurality of network connections. The method includes the process of monitoring the status of each of the remote control computers and of each of the network connections, and selectively switching one network connection to another at a pre-selected interval or as a result of the monitored status of the remote control computers or network connections.

The present invention provides significant advantages over prior art devices and methods. For example, the present invention has a redundancy coupling between

the peripheral devices and the remote control computers. The present invention also includes periodic testing of communication links to detect malfunction and repair.

Additional advantages of the present invention will become readily apparent to those skilled in the art from the following detailed description, wherein only the preferred embodiment of the invention is shown and described, simply by way of illustration of the best mode contemplated for carrying out the present invention. As will be realized, the present invention is capable of other and different embodiments, and its several details are capable of modification in various obvious aspects, all without departing from the present invention. Accordingly, the drawings and description are to be regarded as illustrative in nature, and not as restrictive.

BRIEF DESCRIPTION OF THE DRAWING

Reference is made to the attached drawings, wherein elements having the same reference numeral designations represent like element throughout, and wherein:

Fig. 1 illustrates a block diagram of the multiple port unit of the preferred embodiment;

Fig. 2 illustrates a block diagram of a system architecture according to the preferred embodiment using the multiple port unit of Fig. 1; and

Fig. 3 illustrates a flow chart of an interrogation routine of the preferred embodiment.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

The present invention addresses and solves problems related to the coupling of intelligent electronic devices to one or more remote control computers. More particularly, the present invention relates to a multiple serial port unit which provide a plurality of serial ports connected to the intelligent electronic devices, and which maps the serial ports as communication ports of the remote control computers though redundant communication links.

A preferred embodiment of the invention is schematically illustrated in Fig. 1, which is a multiple port unit 10. Multiple port unit 10 includes power supplies 12 and 14 configured in parallel to operate in a redundant manner. If one of power supplies 12 and 14 fails, the other can function at least temporarily as the sole power supply for multiple port unit 10. A relay contact or other sensing and communication arrangement can be provided to permit indication of a failed power supply to be communicated for alarm or report generation. Power supplies 12 and 14 are coupled to other components of multiple port unit 10 discussed below in an appropriate manner to provide electrical power to the components at the desired voltage and current levels. However, for simplicity of illustration, only a schematic connection between power supplies 12 and 14 and the other components is illustrated.

Processor 16 includes a central processing unit (CPU) and memory. The memory can be of any type including flash memory and EPROM. Processor 16 of the preferred embodiment is a digital device. However, processor 16 can be analog or any arrangement for accomplishing the interrogation routine and other functions described below. Processor 16 can be programmed using known programming languages and methods to accomplish the desired functions.

Communications port module 18 includes interface circuitry and sixteen physical communications ports 20_1 - 20_{16} , (only eight of which are illustrated) such as RS232, RS485, or fiber optic serial ports. Ports 20_1 - 20_{16} each include an appropriate connector, such as a 9-pin D-type connector (DB-9) in the case of RS232 ports, 2 and 4 wire terminal blocks in the case of RS485 ports, and ST connectors in the case of fiber optic serial ports. Alternatively, all or some of ports 20_1 - 20_{16} can be Universal Serial Bus (USB) ports. Each of ports 20_1 - 20_{16} can be of the same type. Alternatively, ports 20_1 - 20_{16} can be of any combination of different types, i.e standards and protocols, of ports depending on the type of intelligent electronic devices to be coupled to multiple port unit 10. Fig. 1 illustrates examples of the types of ports and the corresponding connection to an intelligent electronic device. Port 20_1 is an RS232 port coupled by 3 wire copper cable 30 to intelligent electronic device 32. Port 20_4 is a fiber optic RS232 port and is coupled by a

two optical fibers 40 to intelligent electronic device 42. Port 20₆ is an RS485 port and is coupled by twisted pair copper cable 50 to intelligent electronic device 52. Port 20₈ is an RS232 port and is coupled to intelligent electronic device 66 by modem 62, phone line 63 (POTS), and modem 64. Of course, communications port module 18 can include buffer memory, isolating circuits, signal conditioning circuits, and any other known hardware or software to accomplish the disclosed functions.

Network module 22 includes interface circuitry and two network communications ports 26₁ and 26₂, which are Ethernet ports in the preferred embodiment. Network communications ports 26₁ and 26₂ can include UTP connectors for 10 base-T or 100 base-T Ethernet communications over a twisted pair or can include an ST connector for fiber optic Ethernet communications over optical fibers. Of course, network module 22 can include buffer memory, isolating circuits, signal conditioning circuits, and any other known hardware or software to accomplish the disclosed functions. Network module 22, communications module 18, and processor 16 are communicatively coupled via data bus 24 which can be of any appropriate type, such as an Industry Standard Architecture (ISA), VESA Local Bus (VLB) Peripheral Component Interconnect (PCI) bus, AUI Port or S Bus.

Fig. 2 is a block diagram of a typical computer architecture 70 incorporating plural multiple port units 10₁, 10₂, and 10₃. Architecture 70 includes two control computers 72 and 74 each running a supervisory control program for a plurality of intelligent electronic devices in a redundant manner. The control program can include instructions to control various devices and/or to exchange data with various devices. Computers 72 and 74 can each be a personal computer, a minicomputer, a programmable logic controller, or any other computer capable of supervisory control. In the preferred embodiment, computer 72 is a personal computer (PC1) and computer 74 is a personal computer (PC2). Computer 72 is coupled to Ethernet hub 76 and computer 74 is coupled to Ethernet hub 78. Hubs 76 and 78 are coupled to one another by

Ethernet connection C, which can be accomplished over a twisted pair, optical fiber 10 base 2, or the like.

Three multiple port units 10_1 , 10_2 , and 10_3 , are similar to multiple port unit 10 described above and are coupled to each hub over a separate Ethernet connection. In particular, multiple port unit 10_1 is coupled to hub 76 by Ethernet port 22_1 and link $L1_1$, which can be a twisted pair cable, optical fiber, or the like. Alternatively multiple port unit 10, can be coupled to computer 74 via hub 78 by Ethernet port 22, link $L1_1$, hub 76, connection C, and hub 78. Similarly, multiple port unit 10_1 is coupled to computer 74 via hub 78 by Ethernet port 22_2 and link $L2_1$, which can be a twisted pair cable, optical fiber, or the like. Multiple port units 10_2 , and 10_3 are coupled to hubs 76 and 78 in a similar manner. Corresponding links are labeled with like reference numerals distinguished by the subscript ("1" for multiple port unit 10_1 , "2" for multiple port unit 10_2 , and "3" for multiple port unit 10_3). Intelligent electronic devices 100 (only some of which are labeled in Fig. 2) are coupled to communication ports 20_1 - 20_8 of multiple port units 10_1 , 10_2 , and 10_3 , in the manner described above.

Links $L1_1$ and $L2_1$ are separate Ethernet connections running a separate carrier and separate data packets. Accordingly, the phrase communication link or the word link, as used herein, refers to a communications connection having a carrier and data packets.

Each of communication ports 20_1 - 20_{16} of each multiple port units 10_1 , 10_2 and 10_3 corresponds to a serial communication channel in the preferred embodiment and can be mapped as a COMM port on computers 72 and 74 via TCP/IP over the Ethernet links L_1 and L_2 . Accordingly, each intelligent electronic device can be controlled by, or exchange data with, each of computers 72 and 74. At any given time, the COMM ports can be used by only one of computers 72 and 74. Accordingly, one of computers 72 and 74 is initially configured as the active computer and thereafter an active computer is selected based on interrogation by multiple port units 10_1 , 10_2 , and 10_3 in the

manner described below. In the preferred embodiment, we will assume that computer 72 is set to be the initial active computer. The control program on the active computer can open local COMM ports, which are mapped into the serial communication channels of multiple port units 10₁, 10₂, and 10₃.

5 Accordingly, the active computer can communicate with the intelligent electronic devices as if they were available locally.

Each of multiple port units 10₁, 10₂, and 10₃ periodically interrogates computers 72 and 74 to ascertain their operating status and to switch the active computer if necessary. Also, multiple port units 10₁, 10₂, and 10₃ periodically
 10 switch between links L1 and L2 to test the integrity of both links. If either of links L1 and L2 fails, multiple port unit 10 will send an alarm to computer 72 and/or 74. Therefore, the possibility of switching to a defective link is minimized.

Fig. 3 is a flowchart of the interrogation and control routine carried out
 15 by processor 16 of one of multiple port units 10₁, 10₂, and 10₃. A similar routine is carried out by each of the multiple port units 10₁, 10₂, and 10₃ and thus the routine is only discussed in detail with respect to one of the multiple port units 10₁, 10₂, and 10₃, referred to generally as multiple port unit 10 below.

20 In Fig. 3, steps A2 and A4, the link status variables Link1Status and Link2Status are reset to 1, indicating that both link L1 and link L2 are operational, during either a power up reset of multiple port unit 10 or through an external reset command, not shown. Specifically, a memory location in processor 16 corresponding to these variables is set to 1 indicating that each of
 25 links L1 and L2 are assumed to be active and operating properly. Multiple port unit 10 will then perform an integrity check of link L1 by detecting the Ethernet carrier or Ethernet Link Pulses in step B2. If no Link Pulses are present, then the multiple port unit 10 sets Link1Status to 2 in step A6, to indicate that link L1 is bad, and multiple port unit 10 switches to link L2 in
 30 step L. If Link Pulses are present, multiple port unit 10 sets a Ping Status

variable to 1 in step C. Further in step C, multiple port unit 10 sets Current Link variable to 1 to indicate that link L1 is in use, and variable Link1Status will remain at 1.

In step D, processor 16 determines if it is time to interrogate or "PING" (Packet Internet Groper) computers 72 and 74, i.e. PC1 and PC2, respectively, based on a predetermined interrogation period. For example, processor 16 of the multiple port unit 10 can be programmed to interrogate computers 72 and 74 every 3 seconds. If it is time to interrogate computers 72 and 74, the routine proceeds to step E where processor 16 causes a packet to be sent to the IP address corresponding to each of computers 72 and 74 over link L1. If a reply is received, the corresponding computer 72 or 74 is deemed to be "OK", i.e. operating properly. If no reply is received from one of computers 72 or 74, that computer is deemed to be "bad", i.e. not operating properly.

More specifically, in the routine for checking the status of computers PC1 and PC2 in step E, multiple port unit 10 asks for the number of seconds since midnight from PC1 and PC2. This value should change each time multiple port unit 10 interrogates PC1 and PC2 for this information. When checking for the status of PC1, if the value obtained from PC1 is greater than a previous value, then multiple port unit 10 will set PC1Status to 1, as shown in step G4 or G6, indicating that PC1 is on line. If the multiple port unit 10 cannot get a value from PC1, then the multiple port unit 10 will set the value of PC1Status to 2 indicating that PC1 cannot be reached.

Similarly, if the value obtained by multiple ports unit 10 from PC2 is greater than a previous value, multiple port unit 10 will set the PC2Status to 1, as shown in step G4 or G8, indicating PC2 is on line. If multiple port unit 10 cannot get a value from PC2, then multiple port unit 10 will set PC2Status to 2 in step G6 indicating that PC2 cannot be reached.

If neither PC1 nor PC2 can be reached, as determined in step F2, multiple port unit 10 will set Link1Status variable to 2 in step B4 indicating

that link L1 is bad. Multiple port unit 10 then will switch to link L2 in step L.

The determination in steps F2, H, and G is based on the results of the interrogation step E. If at least one computer can be reached, then link L1 is working properly. If link L1 is working properly, multiple port unit 10 will continue to use link L1, and the routine loops back from step G4, G6, or G8 to step B2 until step K determines it is time to test link L2.

If step K determines that it is time to test link L2, i.e. a preset time for switching links has elapsed, multiple port unit 10 first checks to see if Link2Status variable has been set to 2.

If Link2Status has been set to 2, it indicates that link L2 is bad and switching over to link L2 is not permitted. If Link2Status has been set to 1, as determined in step M2, it indicates that link L2 is operational and switching to link L2 is executed in step M4. In the process of switching to link L2, multiple port unit 10 checks for Ethernet Link pulses in link L2 in step M.

If no Link Pulses are present in link L2, as tested in step M, then multiple port unit 10 sets Link2Status to 2 in step E2 indicating that link L2 is not operational. If link L2 is bad, multiple port unit will check the status of link L1 in step E4 to find a working link to connect to a working computer. If link L1 is also bad, then multiple port unit 10 waits in step D2 for a Linkfixed command from a DDE Client (not shown in the flow chart). When the bad links are determined to be repaired in step D4, the routine moves on to resetting the status of links L1 and L2 by setting Link1Status and Link2Status to 1 indicating that the links are operational. Once the links are repaired the routine will switch multiple port unit to link L1 by returning to step B2.

If Link Pulses are present on link L2, as determined in step M, then multiple port unit 10 sets Ping Status variable to 1 in step N. Also in step N, multiple port unit 10 sets variable Current Link to 2 indicating link L2 is in use, Link1Status to "No Change", and Link2Status to 1. Following step N, the

routine checks to see if it is time for verifying the status of computers PC1 and PC2 in step O. Step O is identical to step D described above for link L1.

When it is time to check computer PC1 and PC2, multiple port unit 10 asks for the number of seconds since midnight from PC1 and PC2 in step P.

- 5 This value should change each time the multiple port unit 10 interrogates PC1 and PC2 for this information. When checking for the status of PC1, if the value obtained from PC1 is greater than a previous value, then multiple port unit 10 will set PC1Status variable to 1, as shown in step G4 or G6, indicating that PC1 is on line. If the multiple port unit 10 cannot get a value from PC1, 10 then multiple port unit 10 will set the value of PC1Status to 2 indicating that PC1 cannot be reached.

- Following step P are steps Q2, S, R, R2, R4, R6, and R8, which are the steps wherein multiple port unit 10 interrogates and controls computers 72 and 74 over link L2 in a manner similar to the interrogation and control conducted 15 over link L1 in steps, F2, H, G, G2, G4, G6, and G8 described in detail above. Accordingly, steps Q2, S, R, R2, R4, R6, and R8 are not described in further detail. The determination in steps Q2, S, and R is based on the result of the interrogation in step P.

- 20 In step Q2, if computers 72 and 74, i.e. PC1 and PC2, are determined to be bad, the procedure will set Ping Status to 1 and Link2Status to 2 indicating that link L2 is bad. Multiple port unit 10 will then switches over to link L1 in step Q6, thereby repeating the above-described routine starting at step B2 for link L1.

- 25 If at least one computer can be reached in step P, then link L2 is working properly. If link L2 is working properly, the multiple port unit 10 will continue to use link L2, and the routine loops back from step R4, R6, or R8 to step M, until step V determines it is time to test link L1.

If step V determines that it is time to test link L1, i.e. a preset time for switching links has elapsed, multiple port unit 10 checks to see if Link1Status variable has been set to 2 in step X2.

5 If Link1Status has been set to 2, it indicates that link L1 is bad and switching over to link L1 is not permitted. If Link2Status has been set to 1, as determined in step V, it indicates that link L1 is operational and switching to link L1 is executed in step Y. In the process of switching to link L1, multiple port unit 10 checks for Ethernet Link Pulses in link L1 in step B2, where the above-described routine repeats itself.

10 The routine described above is carried out by each of multiple port units 10₁, 10₂, and 10₃, in a continuous manner. The routine serves to interrogate each of computers 72 and 74 of their operating status over each of links L1 and L2. If a primary computer does not respond, failure to the other computer is accomplished. Periodically, the Ethernet link is switched and
15 computers 72 and 74 are interrogated again. The routine insures that a working link is used for communications between intelligent electronic devices 100 and computers 72 and 74 and that the primary computer is always OK and updated with all operating variables. Because the interrogation routine and control is carried out by the multiple port units, the interrogation
20 routine does not rely on the integrity of the links or the computers for accuracy.

Interrogation of computers in the preferred embodiment is accomplished by pinging. However, any method of computer interrogation can be used. Interrogation of link status is accomplished by carrier detection,
25 however any method of link interrogation can be used.

Each control computer, e.g. computers 72 and 74, connected to multiple port units 10 comprises a Dynamic Data Exchange (DDE) service that stores and communicates necessary information to each other and to

multiple port units 10 so as to inform controlling architecture 70 the connectivity and operational status data of the computers and links.

Any type of device can be coupled to the communication ports. The communication ports can be of any type. There can be any number of communication ports in each multiple port unit and there can be any number of multiple port units in the architecture subject to addressing limitations. Any number of computers can be used to provide the desired level of redundancy and the interrogation procedure can be modified accordingly. Any number or type of network links can be used to provide the desired level of redundancy and the interrogation procedure can be modified accordingly.

The invention has been described through a preferred embodiment. However, modifications can be made without departing from the scope of the invention as defined by the appended claims.

WHAT IS CLAIMED IS:

1. A multiple port unit adapted for coupling one or more computers to multiple peripheral devices over a network, said multiple port unit comprising:

5 plural network ports, each of said network ports being configured to couple the multiple port unit to a computer over a respective network link;

plural communication ports, each of said communication ports being configured to couple the multiple port unit to a peripheral device; and

10 a control unit configured to interrogate the network links and to communicatively couple said communication ports to a selected one of said network ports based on the interrogation of the network links.

2. A multiple port unit as recited in claim 1, wherein said network ports are configured to couple the multiple port unit to plural computers and wherein said control unit is configured to interrogate each of the plural the
15 computers and to control the peripheral devices based on the interrogation of the computers

3. A multiple port unit as recited in claim 2, wherein said control unit interrogates the computers over each of the network links in an alternating manner.

20 4. A multiple port unit as recited in claim 3, wherein said network ports comprise Ethernet ports.

5. A multiple port unit as recited in claim 4, wherein said communication ports comprise serial ports.

25 6. A multiple port unit as recited in claim 4, further comprising two redundant power supplies.

7. A multiple port unit as recited in claim 1, wherein said control unit is configured to interrogate the network links using a network carrier signal.

8. A multiple port unit as recited in claim 2, wherein said control unit is configured to interrogate the computers using Packet Internet Groper.

9. A multiple port unit as recited in claim 2, comprising two network ports and 8 communications ports.

10. A multiple port unit as recited in claims 2, further comprising a data bus coupled to said control unit, said network ports and said communication ports.

11. A computer architecture comprising:

plural computers;

plural peripheral devices; and

a multiple port unit having plural network ports, plural communication ports, and a control unit, each of said network ports being coupled to one of said plural computers over a respective network link, each of said communication ports being coupled to a peripheral device, said control unit being configured to interrogate the network links and to communicatively couple said communication ports to a selected one of said network ports based on the interrogation of the network links.

12. A computer architecture as recited in claim 11, wherein said control unit is configured to interrogate each of the plural the computers and to control the peripheral devices based on the interrogation of the computers.

13. A computer architecture as recited in claim 12, wherein said control unit interrogates the computers over each of the network links in an alternating manner.

14. A computer architecture as recited in claim 13, wherein said network ports comprise Ethernet ports.

15. A computer architecture as recited in claim 14, wherein said communication ports comprise serial interfaces.

5 16. A computer architecture as recited in claim 14, further comprising two redundant power supplies.

17. A computer architecture as recited in claim 11, wherein said control unit is configured to interrogate said network links using a network carrier signal.

10 18. A computer architecture as recited in claim 12, wherein said control unit is configured to interrogate said computers using Packet Internet Groper.

19. A computer architecture as recited in claim 12, comprising two network ports and 8 communications ports.

15 20. A computer architecture as recited in claim 12, wherein said peripheral devices are intelligent electronic devices.

21. A computer architecture as recited in claim 20, wherein said intelligent electronic devices are protective relays.

20 22. A multiple port unit adapted for coupling one or more computers to multiple intelligent electronic devices over a network, said multiple port unit comprising:

two Ethernet ports, each of said Ethernet ports being configured to couple the multiple port unit to a computer over a respective Ethernet link;

25 plural serial ports, each of said serial ports being configured to couple the multiple port unit to an intelligent electronic device; and

a control unit configured to interrogate the Ethernet links and to communicatively couple said serial ports to a selected one of said Ethernet ports based on the interrogation of the Ethernet links.

23. A multiple port unit as recited in claim 22, wherein said control unit is configured to interrogate each of the plural the computers and to designate a selected one of the computers as an active computer to control the intelligent electronic devices based on the interrogation of the computers.

24. A multiple port unit as recited in claim 23, wherein said control unit interrogates the computers over each of the Ethernet links in an alternating manner.

25. A multiple port unit as recited in claim 24, further comprising two redundant power supplies.

26. A multiple port unit as recited in claim 22, wherein said control unit is configured to interrogate the Ethernet links using an Ethernet carrier signal.

27. A multiple port unit as recited in claim 23, wherein said control unit is configured to interrogate the computers using Packet Internet Groper.

28. A multiple port unit as recited in claim 23, comprising 8 serial ports.

29. A multiple port unit as recited in claim 22, further comprising a data bus coupled to said control unit, said Ethernet ports, and said serial ports.

30. A multiple port unit adapted for coupling one or more computers to multiple peripheral devices over a network, said multiple port unit comprising:

plural network ports, each of said network ports being configured to couple the multiple port unit to a computer over a respective network link;

plural communication ports, each of said communication ports being configured to couple the multiple port unit to a peripheral device; and

control means for interrogating the network links and communicatively coupling said communication ports to a selected one of said network ports based on the interrogation of the network links.

31. A multiple port unit as recited in claim 30, wherein said network ports are configured to couple the multiple port unit to plural computers and wherein said control means comprises computer interrogating means for interrogating each of the plural computers and designating a selected one of the computers as an active computer to control the peripheral devices based on the interrogation of the computers.

32. A multiple port unit as recited in claim 31, wherein said computer interrogating means interrogates the computers over each of the network links in an alternating manner.

33. A multiple port unit as recited in claim 32, wherein said network communication ports comprise Ethernet ports.

34. A multiple port unit as recited in claim 33, wherein said communication ports comprise serial ports.

35. A multiple port unit as recited in claim 33, further comprising two redundant power supplies.

36. A multiple port unit as recited in claim 30, wherein said control means comprises means for detecting a network carrier signal.

37. A multiple port unit as recited in claim 31, wherein said computer interrogation means comprises Packet Internet Groper.

38. A multiple port unit as recited in claim 30, further comprising a data bus coupled to said control means, said network ports and said serial ports.

39. A multiple port unit as recited in claim 31, comprising two network ports and 8 communications ports.

40. A method of coupling plural peripheral devices to computers, said method comprising the steps of:

5 interrogating the status of plural network connections with a control unit of a multiple port unit having plural network ports coupled to the plural network connections and plural communication ports coupled to peripheral devices; and

 coupling the plural communication ports to one of the network
10 connections based on the results of said step of interrogating the status of plural network connections.

41. A method as recited in claim 40 further comprising the steps of interrogating the status of plural computers respectively coupled to the network connections; and controlling the peripheral
15 devices based on the results of said step of interrogating the status of plural computers.

42. A method as recited in claim 41, wherein said step of interrogating the status of plural network connections comprises detecting a carrier on each network connection.

20 43. A method as recited in claim 41, wherein said step of interrogating the status of plural computers comprises using Packet Internet Groper.

 44. A method as recited in claim 41, further comprising the step of
25 maintaining a record of the status of each computer and each network connection in the control unit.

45. A method as recited in claim 41, further comprising the step of transferring status data between the computers at the direction of the control unit.

COMPUTER MULTIPLE COMMUNICATIONS PORT UNIT

ABSTRACT OF THE DISCLOSURE

5 A multiple port unit includes plural peripheral communications ports and two network ports for connect the peripheral devices with at least one remote controlling computer. Multiple port unit further includes a controller which interrogates network links coupled to the network ports and alternately switching network links. The controller also interrogates computers coupled to the network links and ascertains that a computer controlling devices coupled to the communication ports is working properly.

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Fig. 1

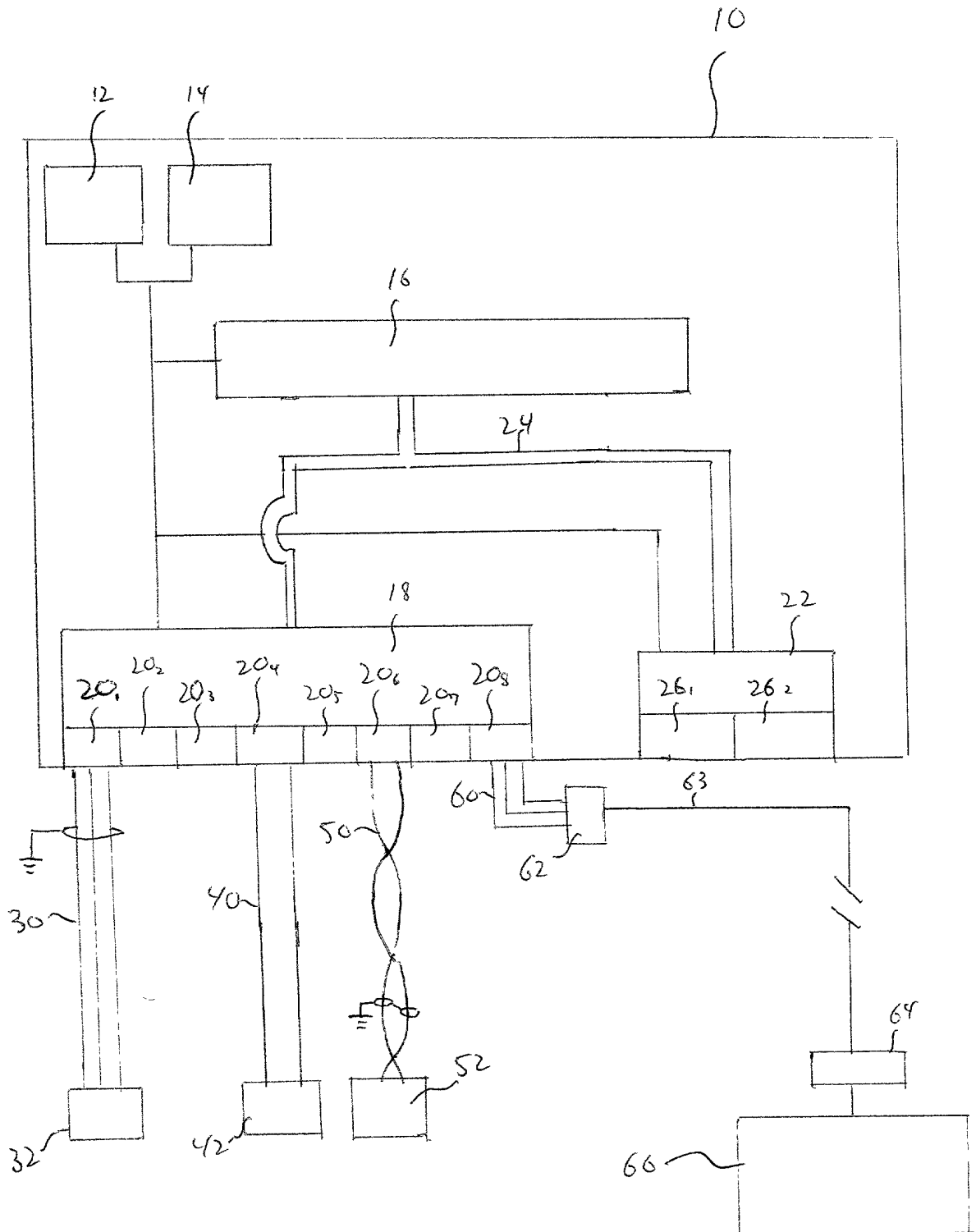
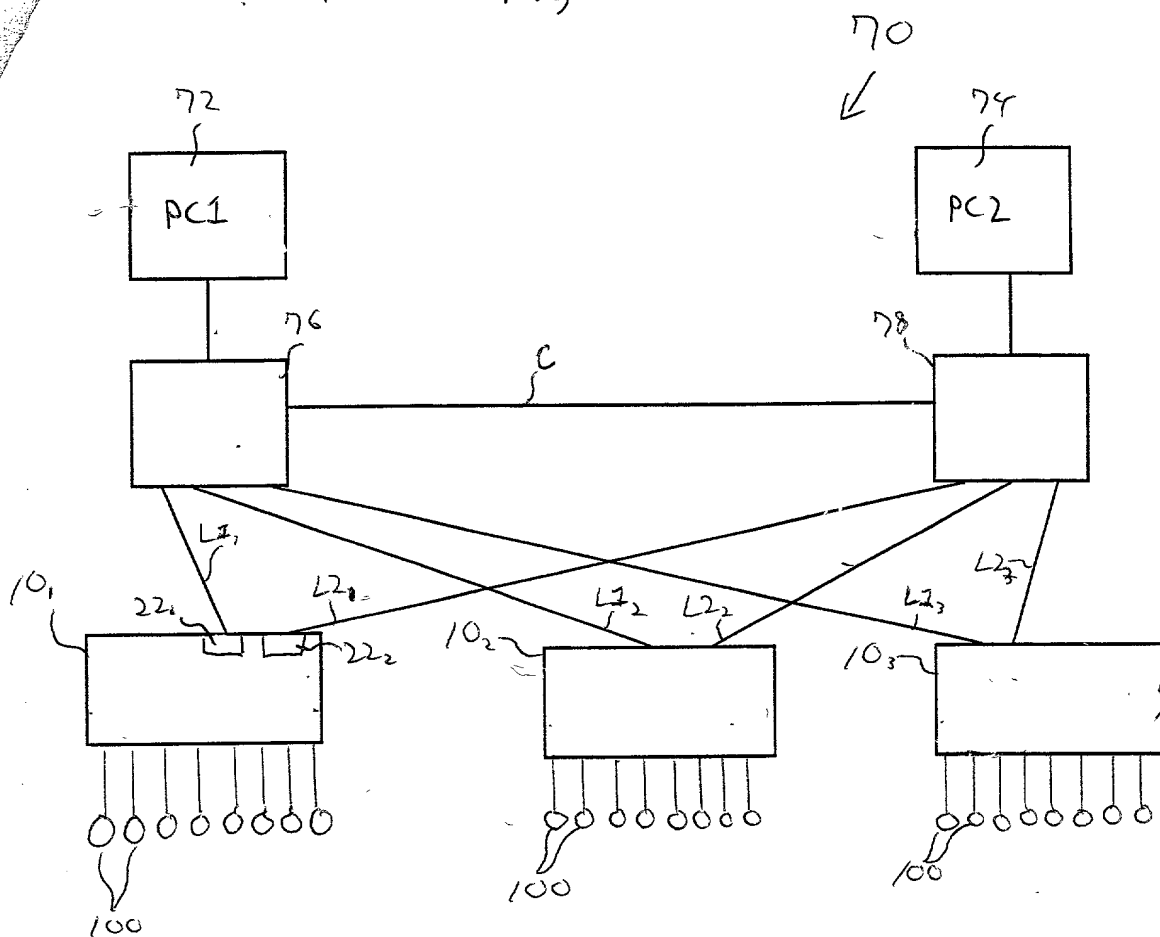


Fig. 2



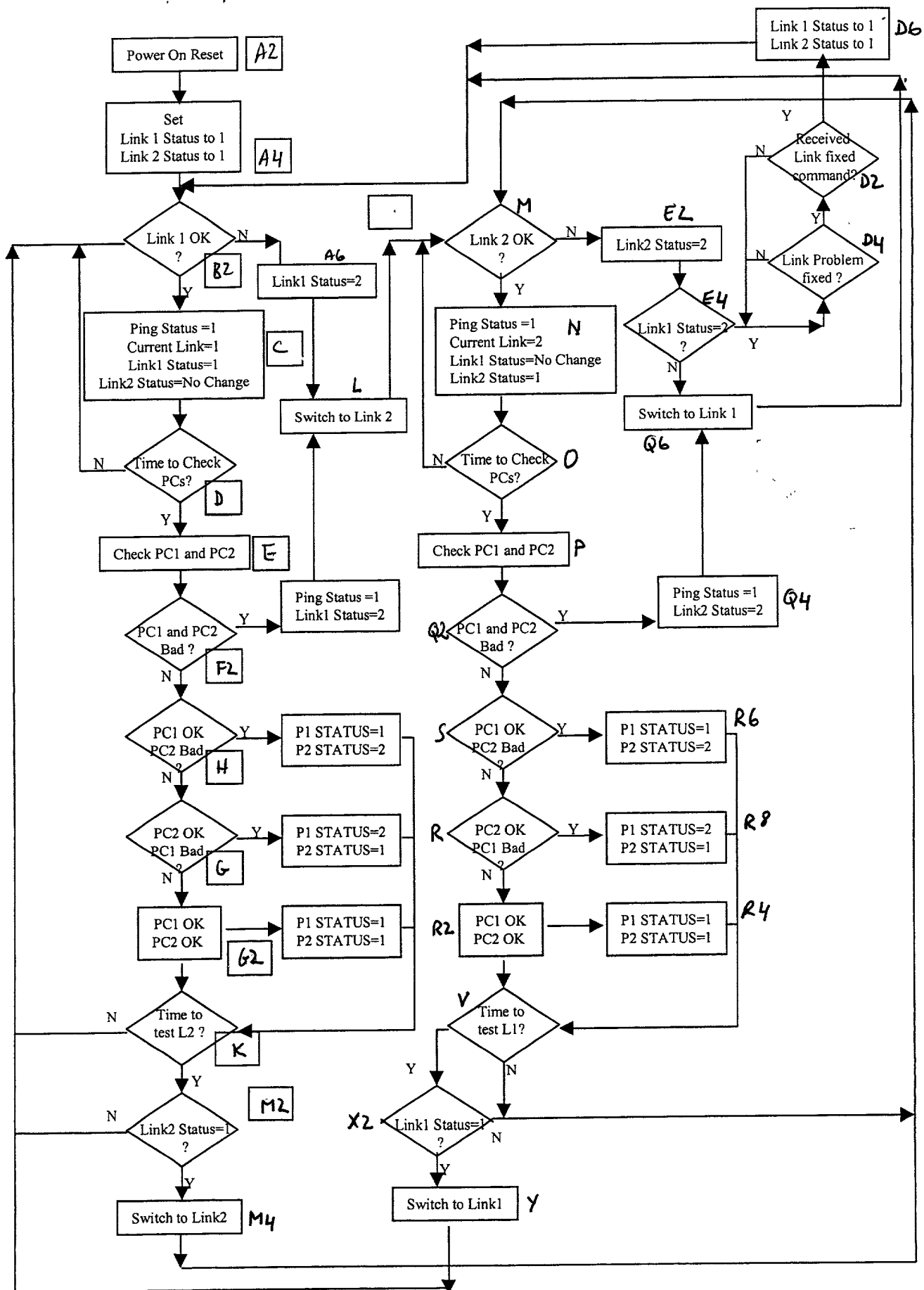


FIG. 3

**COMBINED DECLARATION AND
POWER OF ATTORNEY FOR PATENT APPLICATION**

As a below-named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below my name.

I believe I am an original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention "**COMPUTER MULTIPLE COMMUNICATIONS PORT UNIT**", Docket No. 6202-0041 and 11RC-4937 the specification of which is attached hereto unless the following box is checked:

☐ The specification was filed on _____
and was assigned Serial No. _____
(if known)
and was amended on _____
(if applicable)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the U.S. Patent and Trademark Office all information known to me which is material to patentability (as defined in 37 C.F.R. Sec. 1.56) in connection with the examination of this application. I also acknowledge the duty to disclose all information known to me to be material to patentability which becomes available between the date of the prior application and the national or PCT international filing date of any continuation-in-part application.

I hereby claim the benefit under Title 35, United States Code, §119(e) or §120, as applicable, of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

Application Serial No.	Filing Date	Status: patented, pending, abandoned

I hereby appoint Robert M. Schulman, Reg. No. 31,196, Marc S. Kaufman, Reg. No. 35,212, Corinne R. Gorski, Reg. No. 34,339, Donald R. Studebaker, Reg. No. 32,815, Tim L. Brackett, Jr., Reg. No. 36,092, Brian C. Oakes, Reg. No. 41,467, James E. Howard, Reg. No. 39,715 and Jason H. Vick, Reg. No. 45,285 all of NIXON PEABODY LLP and Ronald E. Myrick, Reg. No. 26,315, Henry J. Policinski, Reg. No. 26,621, Jay L. Chaskin, Reg. No. 24,030, Henry I. Steckler, Reg. No. 24,139 and James W. Mitchell, Reg. No. 25,602, all of GENERAL ELECTRIC COMPANY, 3135 Easton Turnpike, Fairfield, CT 06431, Carl B. Horton, Reg. No. 34,622, Damian G. Wasserbauer, Reg. No. 34,749, Wayne O. Traynham, Reg. No. 29,872 and Dave S. Christensen, Reg. No. 40,955, all of GENERAL ELECTRIC COMPANY, 41 Woodford Avenue, Plainville, CT 06062, jointly, and each of them severally, my attorneys and attorney, with full power of substitution, delegation and revocation, to prosecute this application, to make alterations and amendments therein, to receive the patent and to transact all business in the Patent and Trademark Office connected therewith, I hereby direct that all correspondence and telephone calls in connection with this application be addressed to the said Robert M. Schulman at NIXON PEABODY LLP, 8180 Greensboro Drive, Suite 800, McLean, VA 22102, Telephone No. (703) 790-9110.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that all such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of first inventor: PUI LUN LAU

Inventor's Signature: *Pui Lun Lau* X Date: 10/30/2000 X

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